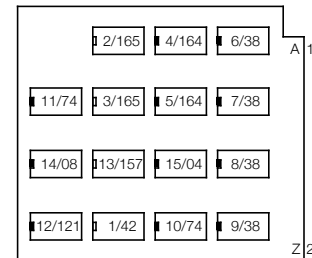


**I/O Bus Edge Connector**  
(as viewed at top of socket)

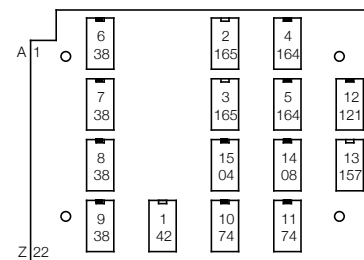
NOY			
GND	1	A	<
V+5	2	B	<
⌈0	3	C	nEDT
MCK	4	D	-
nWFM	5	E	SCK
nDI0	6	F	DO0
DO1	7	H	DO2
nDI3	8	J	DO3
nDI2	9	K	nDI1
DO4	10	L	nDI4
DO5	11	M	nDI5
DO6	12	N	nDI6
DO7	13	P	nDI7
SO0	14	R	nSI0
SO1	15	S	nSI1
SO2	16	T	nSI2
SO3	17	U	nSI3
CO0	18	V	CO1
CO2	19	W	CO3
nSSI	20	X	nSIH
nCEOb	21	Y	nCFI
GND	22	Z	nKSTOP

**Physical Layout  
Unit #1**



- Alterations on Unit #1:
- U9b pull-up R is 3.57K.
  - 10K resistors are 11K installed.
  - 3 external-input pull-up R and D clamps are absent.
  - 8 decoupling capacitors.

**Physical Layout  
Unit #2**



- Alterations on Unit #2:
- 2 internal pull-up resistors are 3.57K.
  - 10K resistors are 11K installed.
  - U15 a,b units are swapped.

- 2014 Jan: Design, unit #1 constructed / bhiipert.
- 2014 Mar: Unit #2 constructed.
- 2014 Apr: Rev 1.1. Problem: Glitch from U13a clears ACK, appeared with 74LS157 IC with different characteristics. Fixed by removing U13a select of ACK reset.
- 2014 Apr: Rev 1.2. 1.1 fix introduces new problem, TLIST does not work, ACK now is cleared because firmware HALT routine does not deassert CEO, ACK flag is held in reset while host tries to set it. U9b moved to control ACK reset. Must rely on host to always restore ACK for tape operations.