


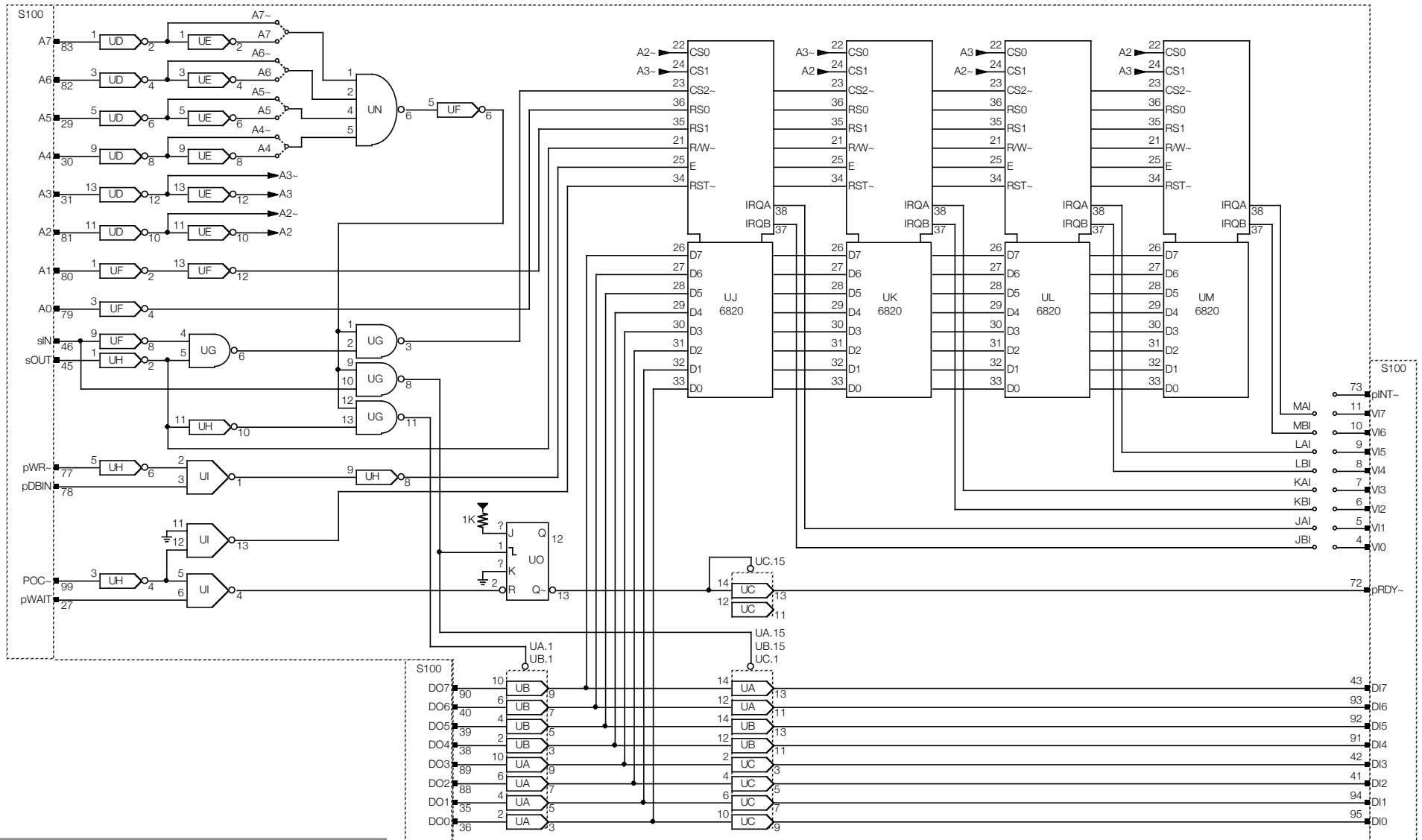
**MIT S
88-4PIO**

S100 Parallel IO Board

Schematic

NOTES

- The symbol  represents a connector pin. The solid black end is the male side, the white-filled end is the female side.
Connector pins are identified by a label of the form:
N <connector> . <pin>
- A small black marker on the upper half of a gate symbol indicates an open-collector output.
- Based on MITS schematic.
- 2023Mar: This drawing / bhilpert.

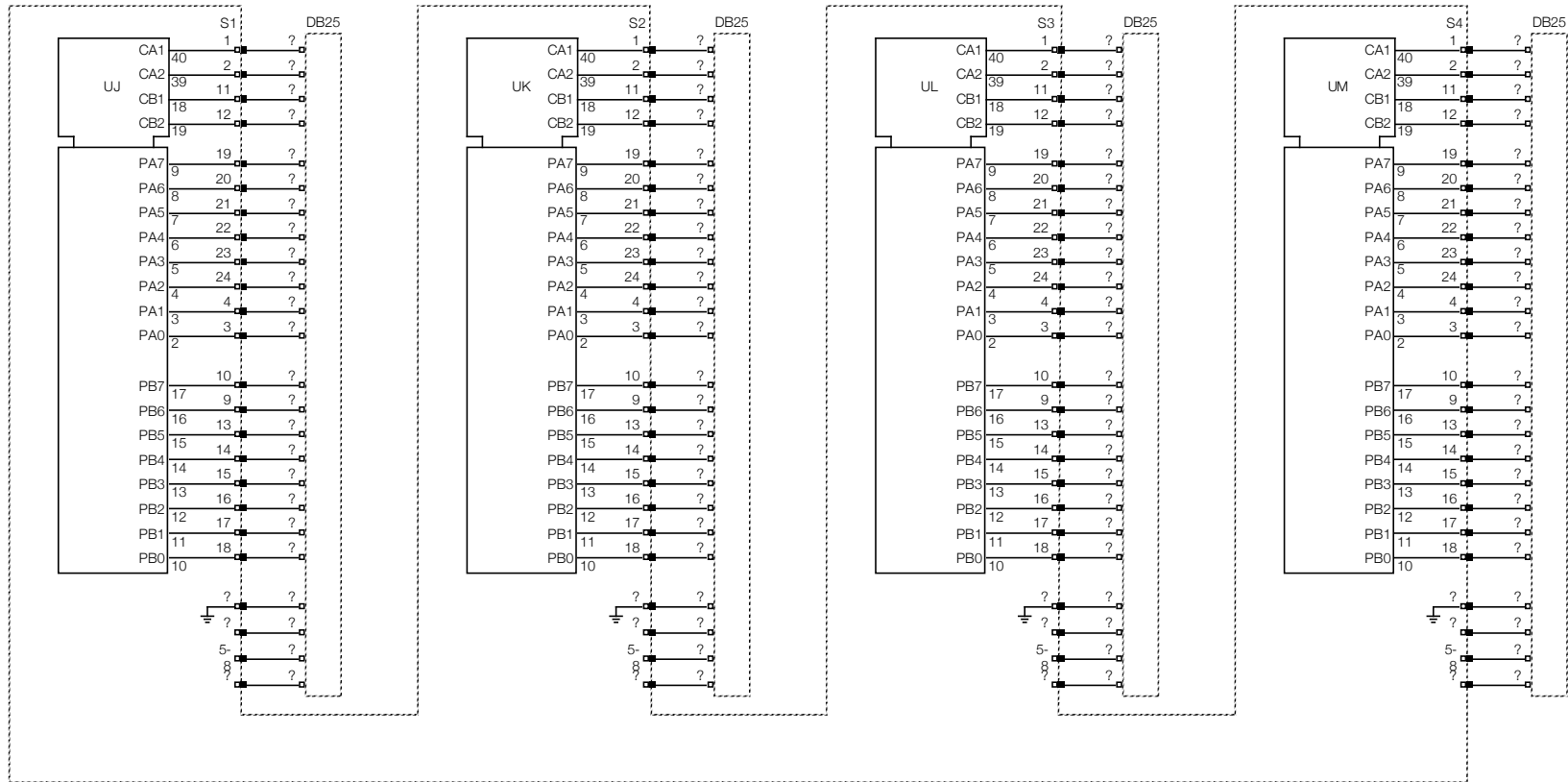


MITS 88-4PIO S100 Serial IO Board

Section: Bus & Control

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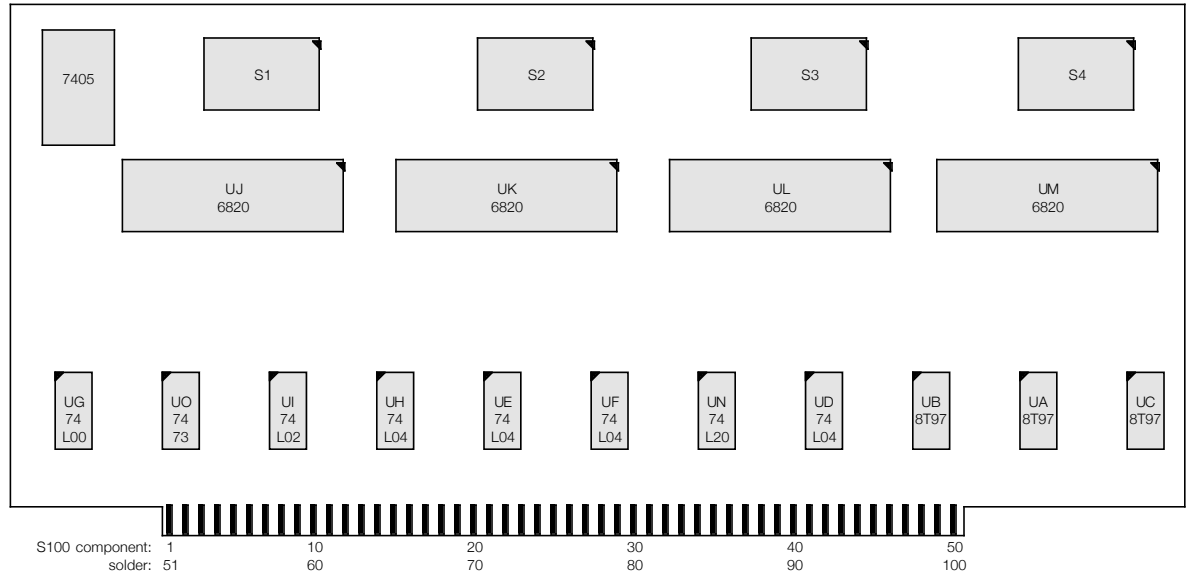
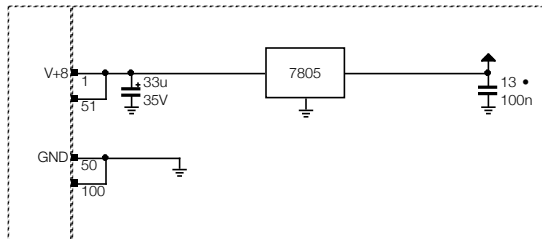


MITS 88-4PIO S100 Serial IO Board

Section: Interface

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Ux	Type	a	b	c	d	e	f
A	8T97	√	√	√	√	√	√
B	8T97	√	√	√	√	√	√
C	8T97	√	√	√	√	u	√
D	74L04	√	√	√	√	√	√
E	74L04	√	√	√	√	√	√
F	74L04	√	√	√	√	u	√
G	74L00	√	√	√	√		
H	74L04	√	√	√	√	√	u
I	74L02	√	√	u	√		
J	6820	√					
K	6820	√					
L	6820	√					
M	6820	√					
N	74L20	√	u				
O	7473	√	u				

Type	Qty	Pins	V+5	GND
6820	4	40	20	1
74L00	1	14	14	7
74L02	1	14	14	7
74L04	4	14	14	7
74L20	1	14	14	7
7473	1	14	14	7
8T97	3	16	16	8
Total:	15			

Total ICs: 15

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Section: Layout & IC Types

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