Microtel VTX-202
Telidon Terminal
Schematic

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**NOTES**

- The symbol ✗ represents a connector pin. The solid black end is the male side, the white-filled end is the female side.
- A small black rectangular marker on the upper half of a gate symbol indicates an open-collector output.
- Capacitance in microfarads unless otherwise noted.
- As received, U31 configuration jumpers 2, 4 & 8 were closed.
- This schematic is based on a reverse engineering of unit with Serial No. 569.
  Component date codes in this unit are primarily 1981.

**NOTE**

This schematic has been derived through examination of the equipment.
This is not the manufacturer’s schematic.

bhilpert / 2015
Microtel VTX-202 Telidon Terminal

Section: Extended Memory, Memory Map
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- Extended Memory RAM is 256 bytes overmapped into the region B800 to EFFF.
- The Extended Memory bus is enabled in the region 8000 to 9FFF, but there is no memory there to respond.
- U31 switches 7 & 8 control the mapping of the ROMs and Extended Memory in the region A000 to DFFF.

However there is once again no actual Extended Memory present in that region.

- I/O devices are overmapped into the region F000 to FFFF, with base addresses:
  - F080  Comm. Control/Status
  - F081  Comm. Data
  - F100  PA Keyboard Data
  - F101  PA Keyboard Control
  - F102  PA Misc. Data
  - F103  PA Misc. Control
  - F180  CRT Address
  - F181  CRT Data

- The CRT Address is 8000 to 8007.
- The CRT Data is 8008 to 800F.
- The upper 8 KBytes of I/O space is overmapped into the range E000 to EFFF.

Memory Map:

- ROMS
- 128K Bytes of ROM: 1.03.01B
- 32K Bytes of ROM: 1.03.01A
- EMAM
- EPROM
- Extended Memory: 32K Bytes

- CRT Address
- CRT Data
- EM RAM: 256 Bytes
- I/O: 8 Bytes
- UART: 16 Bytes
- Memory: 8K Bytes
- MAP: 8K Bytes
- 16K Bytes: 1.03.01A
- 128K Bytes: 1.03.01B
- RAM: 8K Bytes
- ROM: 128K Bytes
- EMAM: 32K Bytes

- 128K Bytes: M3-6551
- 256 Bytes: 2716
- 4K Bytes: M3-6551
- D000 to DFFFF:

However there is once again no actual Extended Memory present in that region.